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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): David Dice et al.

Title: **QUICKLY REACQUIRABLE LOCKS**

Application No.: 10/669,948 Filed: September 24, 2003

Examiner: William D. Thomson Group Art Unit: 2194

Atty. Docket No.: 004-8728 Confirmation No.: 8314

December 28, 2005

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37 C.F.R. § 1.97(b)

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- Form(s) PTO/SB/08B (1 page(s), with copy of 15 reference(s)).
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- This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
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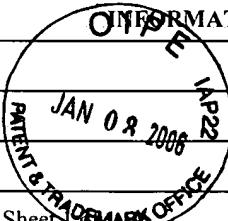
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Respectfully submitted,



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PTO/SB/08B (Substitute for form 1449/PTO)		Attorney Docket No.: 004-8728
		Application No.: 10/669,948
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		First Named Inventor: David Dice
		Filing Date: September 24, 2003
		Group Art Unit: 2194
Sheet 1 of 1		Examiner Name: William D. Thomson
		Date Submitted: December 28, 2005

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	MASSALIN, H., et al., "A Lock-Free Multiprocessor OS Kernel," Technical Report TR CUCS-005-9, Columbia University, New York, NY, 1991, 21 pages.	
	2	MASSALIN, Henry, "Synthesis: An Efficient Implementation of Fundamental Operating System Services," Dissertation submitted in partial fulfillment of the requirements for the Degree of Doctor of Philosophy in the Graduate School of Arts and Sciences, Columbia University, New York, NY, online, 158 pages, 1992 [retrieved from the Internet on 2001-07-13: URL:ftp://ftp.cs.columbia.edu/reports/reports-1992/cucs-039-92.ps.gz].	
	3	BERSHAD, B. N., "Practical Considerations For Non-Blocking Concurrent Objects," <i>Proceedings 13th IEEE International Conference on Distributed Computing Systems</i> , pp. 264-273. IEEE Computer Society Press, Washington, D.C., 1993.	
	4	HERLIHY, M., "A Methodology For Implementing Highly Concurrent Data Objects," <i>ACM Transactions on Programming Languages and Systems</i> , 15(5):745-770, November 1993.	
	5	ATTIYA, Hagit, et al., "Are Wait-Free Algorithms Fast?" <i>Journal of the ACM</i> , 41(4):725-763, July 1994.	
	6	LAMARCA, A., "A performance evaluation of lock-free synchronization protocols," <i>Proceedings of the 13th Annual ACM Symposium on Principles of Distributed Computing</i> , pp. 130-140, ACM Press, New York, NY, 1994.	
	7	MICHAEL, Maged M. et al., "Simple, Fast, and Practical Non-Blocking and Blocking Concurrent Queue Algorithms," <i>Proceedings of PODC</i> , pp. 267-275, May 1996.	
	8	ATTIYA, H., et al., "Universal Operations: Unary versus Binary," <i>Proceedings of the 15th Annual ACM Symposium on Principles of Distributed Computing</i> , pp. 223-232, ACM Press, New York, NY, 1996.	
	9	GREENWALD, M. B., et al., "The Synergy Between Non-Blocking Synchronization And Operating System Structure," <i>Proceedings of the 2nd Symposium on Operating Systems Design and Implementation</i> , pp. 123-136, Usenix Association, Berkeley, CA, 1996.	
	10	AFEK, Y., et al., "Disentangling Multi-Object Operations," <i>Proceedings of the 16th Annual ACM Symposium on Principles of Distributed Computing</i> , pp. 111-120, August 1997. Santa Barbara, CA.	
	11	ARORA, N. S., et al., "Thread Scheduling For Multiprogrammed Multiprocessors," <i>Proceedings of the 10th Annual ACM Symposium on Parallel Algorithms and Architectures</i> , pp. 119-129, ACM Press, New York, NY, 1998.	
	12	ATTIYA, Hagit, et al., "Atomic Snapshots In O(n log n) Operations," <i>SIAM Journal on Computing</i> , 27(2):319-340, April 1998.	
	13	GREENWALD, M., "Non-Blocking Synchronization and System Design," PhD thesis, Stanford University Technical Report STAN-CS-TR-99-1624, Palo Alto, CA, August 1999, 241 pages.	
	14	AGESEN, Ole, et al.: "DCAS-BASED CONCURRENT DEQUES," <i>SPAA 2000. Proceedings of the 12th Annual ACM Symposium on Parallel Algorithms and Architectures</i> , pp. 137-146, ACM Press, New York, NY, ISBN: 1-58113-185-2, 2000.	
	15	DETLEFS, David L., et al., "Even Better DCAS-Based Concurrent Deques," <i>Lecture Notes in Computer Science</i> , vol. 1914, pp. 59-73, Springer-Verlag, Berlin, Germany, ISBN: 3-540-41143-7, 2000.	

Examiner Signature		Date Considered	
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